



FACULTY DEVELOPMENT PROGRAMME (FDP) ON

# MACHINE LEARNING AND ITS APPLICATIONS IN SIGNAL PROCESSING

(23<sup>rd</sup>-28<sup>th</sup> December, 2019)

Organized by

**E & ICT Academy, National Institute of Technology, Warangal**  
at

**Bharat Institute of Engineering and Technology, Hyderabad**

(Sponsored by Ministry of Electronics and Information Technology (MeitY), GOI)



## Preamble:

“Electronics & ICT Academy” was set up at NIT Warangal with financial assistance from MeitY, Govt. of India (GoI). The jurisdiction of this academy is Telangana, Andhra Pradesh, Andaman and Nicobar Islands, Goa, Pondicherry and Karnataka states/UTs. This academy role is to offer faculty development programmes in standardized courses and emerging areas of Electronics, Information Communication Technologies, Curriculum development for Industry, Training & Consultancy services for Industry; CEP for working professionals, Advice and support for technical incubation and entrepreneurial activities.

As part of this initiative, a Faculty Development Programme (FDP) on “Machine Learning” is being conducted at Bharat Institute of Engineering and Technology, Hyderabad for the Faculty members of engineering colleges, industries and research laboratories.

Machine Learning Usually plays an important role in the transition from data storage to decision systems based on large databases signals such as the obtained from sensor networks, internet services, or communication systems. These systems imply developing both computational solutions and novel models. The aim of the workshop is to contribute to the cross fertilization between the research on Machine Learning methods and their applications to Signal Processing.

## Major Course Contents:

- Introduction to Machine Learning
- Python Fundamentals
- Signal Processing and its Applications
- Convolutional Neural Networks for Audio and Video Processing
- Deep Architectures Modelling and Optimization
- Machine Learning for Networking
- Machine Learning for Speech Processing
- Application Building using Deep Architectures
- Pattern Recognition and Image Processing

Hands on session on the above topics will be provided.

## Faculty Conducting this Programme:

The programme will be conducted by the faculty members from NIT Warangal. Academicians from IITs/NITs/IIITs in concerned field are invited to deliver lectures in the programme. Speakers from industries are also expecting to deliver lectures.

## Eligibility:

The programme is open to the faculty of engineering colleges and other allied disciplines in India. Industry personnel working in the concerned/allied discipline can also attend.

## Organized at:

The programme will be coordinated and conducted by ECE department, Bharat Institute of Engineering and Technology, Hyderabad, Telangana.

## Accommodation:

No TA/DA will be paid to the participants. Accommodation is arranged for outstation participants on PAYMENT Basis, Working Lunch, Tea & Snacks would be provided during the training at BIET campus, on PAYMENT Basis.

**Venue: Pharmacy Hall & Computer Lab, Bharat Institute of Engineering and Technology, Hyderabad.**

## Registration Fee Particulars:

- Faculty/Research Scholars : Rs. 2,500/- Only
- Industry participants : Rs. 7,500/- Only

Participants need to pay the Registration fee either in the form of Demand Draft (DD) or Online Transfer using the following details

DD Details	Online Transfer Details
Demand Draft infavor of “Electronics & ICT Academy NITW, NIT Warangal” payable at any bank in Warangal	Account Name: Electronics & ICT Academy NITW Account No.: 62423775910 Name : State Bank of India Branch: REC Warangal IFSC: SBIN0020149

## How to Apply:

A filled in form of application in the prescribed format duly signed and sponsored by appropriate authorities (along with DD/Online Transfer Receipt) should reach the coordinator by speed post. It is also mandatory to send scanned application form & DD/ Online Transfer receipt through email to [papivadutta@biet.ac.in](mailto:papivadutta@biet.ac.in) as selection will be intimated only through mail.

## Selection Criteria:

Selection will be done based on *first-cum-first-serve basis* and the confirmed candidates will be notified immediately. The maximum number of participants will be **50 (fifty)**. Additionally 10 participants from industry are allowed to participate. The list of selected participants will be sent to their personal *e-mail* ids. In case a candidate is not selected, the demand draft will be sent back. A test will be conducted at the end of the course. Candidates will be issued satisfactory certificate on successful completion of the course. Reservations are followed for selecting candidates as per GOI norms.

## Important Dates:

Last date for submission of application : **14<sup>th</sup> December, 2019**

Selection-list intimation/display before : **16<sup>th</sup> December, 2019**

Duration of Program : **23<sup>rd</sup> to 28<sup>th</sup> Dec, 2019**

## About NIT Warangal:

National Institute of Technology (formerly Regional Engineering College), Warangal is the first among 17 RECs setup as joint venture of the Government of India and the state government. Over the years the college has established itself as a premier Institution imparting technical education of a very high standard leading to the B.Tech degrees in various branches of engineering, M.Tech & Ph.D programs in various specializations.

## About BIET Hyderabad:

Bharat Institute of Engineering & Technology (BIET), established in 2001 by Chinta Reddy Madhusudhan Reddy Educational Society (CRMR) under the leadership of Sri Ch. Venugopal Reddy, an eminent educationist with a social conscience and commitment. It has grown in its size and stature over the years, BIET is one of the largest Engineering Colleges in Telangana. It offers 6 B.Tech programs, 9 M.Tech programs and 1 MBA program. Bharat Institute of Engineering & Technology (BIET) is NAAC Accredited institution with well experienced and highly qualified faculty, State-of-the-art Infrastructure, regular placements and well equipped laboratories.



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## APPLICATION FORM

1. Name of the Applicant:
2. Designation :
3. Institution :
4. E-mail ID :
5. Mobile No. :
6. Category
  - Faculty/Scholars (Rs. 2,500/-)  DD /Online Transfer Receipt No: \_\_\_\_\_
  - Industry Participants (Rs. 7,500/-) Bank: \_\_\_\_\_ Date : \_\_\_\_\_
7. Payment Mode
8. Address for Correspondence:
9. Subjects taught so far:
10. Educational Qualifications with specialization:
11. No .of refresher courses/workshops attended:
12. Experience (in years) :  Teaching  Research  Industry
13. Do you require accommodation during Training : YES/NO
14. Do you belong to SC  ST

### Declaration

The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the FDP and shall attend the course for the entire duration. I also undertake the responsibility to inform the Coordinator in case, I am unable to attend the course.

Place :

Date :

**Signature of the Applicant**

### SPONSORSHIP CERTIFICATE

Dr./Mr./Ms.....is an employee of our Institute/Organization and is hereby sponsored to participate in the FDP on “**Machine Learning Applications in Signal Processing**” sponsored by **Electronics & ICT Academy** 23<sup>rd</sup> to 28<sup>th</sup> December, 2019 at Bharat Institute of Engineering and Technology, Hyderabad, Telangana.

Place :

Date :

**Signature of Principal/Head of Institution**

**Address for correspondence**

**Post your application form along with DD/NEFT Receipt to**

**Dr. Papiya Dutta**

Associate Professor,

Dept. of ECE

**Bharat Institute of Engineering and Technology,**  
Hyderabad, TS- 501 510.

**Email: [papiyadutta@biet.ac.in](mailto:papiyadutta@biet.ac.in)**

**Mobile: 9827587253**

#### Coordinators:

**Dr. T. Kishore Kumar**

Professor

Dept. of ECE

NIT Warangal

Telangana

**Dr. Papiya Dutta**

Associate Professor

Dept. of ECE

BIET, Hyderabad

Telangana

For more details about Electronics and ICT academy please visit: <http://www.nitw.ac.in/eict/>