



**NATIONAL INSTITUTE OF TECHNOLOGY**  
**WARANGAL – 506 004 (Telangana State)**  
**DeitY, HRD division, Ministry of ICT, Govt. of India SPONSORED**  
**Faculty Development Programme (FDP)**

On

**Information Theory and Coding-Applications**

Organized by

**Electronics & ICT Academy**  
**and Department of Electronics and Communication Engineering**  
**(21st Nov 2018 – 26th Nov 2018)**

**Preamble:**

"Electronics & ICT Academy" was set up at NIT Warangal with financial assistance from MeitY, GoI. The jurisdiction of this academy is Telangana, Andhra Pradesh, Karnataka, Goa, Puducherry and Andaman & Nicobar Islands. This academy role is to offer faculty development programmes in standardized courses and emerging areas of Electronics, Information Communication Technologies, training & consultancy services for Industry, Curriculum development for Industry, CEP for working professionals, Advice and support for technical incubation and entrepreneurial activities.

**Major Course Contents:**

- Introduction to Information Theory for Discrete Variables: Entropy, Mutual Information.
- Information Theory Inequalities
- Coding of Discrete Memoryless Sources
- Coding of Discrete Stationary Sources
- Channel Capacity, Channel Models
- Source Coding Techniques
- Review of Block codes and cyclic codes
- LDPC block codes and codes on graphs
- BCH and RS codes
- Convolutional codes
- Turbo codes
- Applications.

**Faculty conducting this programme:**

The programme will be conducted by the faculty members from **NIT Warangal**. Academicians in the concerned field from **IITs/NITs/IIITs** are invited to deliver lectures in the programme. Speakers from **industries** are also expected to deliver as part of the course.

**Eligibility:** The programme is open to faculty of Engineering Colleges, MCA Colleges and other allied disciplines in India. Industry personnel working in the concerned /allied discipline can also attend.

**Registration Fee Particulars:**

Faculty and Research Scholars from Telangana, Andhra Pradesh, Karnataka, Puducherry, Andaman and Nicobar Islands, Goa	Rs. 2500/-
Faculty of SC/ST Category from Telangana, Andhra Pradesh, Karnataka, Puducherry, Andaman and Nicobar Islands, Goa	Rs. 1875- (SC/ ST participants should submit the copy of their caste certificate to claim the concession along with application form)
Industry Participants	Rs. 7500/-
Participants from other states	Rs. 8500/-

**SC/ST concession is only for faculty of mentioned states. Research Scholars are not eligible for SC/ST concession.**

The fee is to be paid either in the form of DDs or online transfer using the following details:

DD Details	Online Transfer Details
Demand Draft in favor of "Director, NIT Warangal" payable at any bank in Warangal	Account Name: Electronics & ICT Academy NITW Account No: 62423775910 IFSC: SBIN0020149

**Accommodation:**

All the selected participants will be provided FREE boarding & lodging in the institute guest house. No TA will be paid for the participants.

**How to apply:**

A filled in form of application in the prescribed format duly signed and sponsored by appropriate authorities (along with demand draft) should reach the coordinator by speed-post. It is also mandatory to send scanned application form and demand draft through e-mail to [anuradha@nitw.ac.in](mailto:anuradha@nitw.ac.in) as selection will be intimated only through mail.

**Selection Criteria:**

Selection will be done based on *first-cum-first-serve basis* and the confirmed candidates will be notified immediately. The maximum number of participants will be **50 (fifty)**. Additionally 10 participants from industry are allowed to participate. The list of selected participants will be notified in the institute web site [www.nitw.ac.in](http://www.nitw.ac.in) and also will be sent to their personal e-mail ids. In case a candidate is not selected, the demand draft will be returned. A test will be conducted at the end of the course. Candidates will be issued certificates on successful completion of the course along with grade. Reservations are followed for selecting candidates as per GOI norms.

**Important dates:**

Last date for submission of application: 12.11.2018

Selection-list intimation/display before: 15.11.2018

Duration of Program: 21<sup>st</sup> Nov 2018 – 26<sup>th</sup> Nov 2018

**About the Institute, department and Warangal:**

National Institute of Technology (formerly Regional Engineering College), Warangal is the first among 17 RECs setup as joint venture of the Government of India and the state government. Over the years the college has established itself as a premier Institution imparting technical education of a very high standard leading to the B.Tech degrees in various branches of engineering and M.Tech. and Ph.D programs in various specializations.

The Department of Electronics and Communication Engineering offers B.Tech. course in ECE, M.Tech courses in Advanced Communication Systems (ACS), VLSI design and Electronic Instrumentation. The Department has experienced faculty with good publications and well-established laboratories. The Department has liaison with reputed industries and R&D organizations.

Warangal is known for its rich historical and cultural heritage. It is situated at a distance of 140Km. from Hyderabad. Warangal is well connected by rail and road. National Institute of Technology campus is 2 Km. away from Kazipet station and 12Km. away from Warangal station.

**FORMAT OF APPLICATION**  
**Electronics and ICT Academy**  
**Faculty Development Programme**  
on  
Information Theory and Coding-Applications

(21<sup>st</sup> Nov 2018 – 26<sup>th</sup> Nov 2018)

**SPONSORSHIP CERTIFICATE**

1. Name : Dr/Mr/Ms. .... is  
2. Designation : ..... is  
3. Institution : an employee of our Institute/Organization and is hereby  
4. Email & Mobile No: sponsored to participate in the FDP on signal processing  
5. DD No: Bank: Date: for communications sponsored by **Electronics & ICT Academy** during 21<sup>st</sup> Nov 2018 – 26<sup>th</sup> Nov 2018 at  
6. Address for Correspondence National Institute of Technology, Warangal

Place:

Date:

7. Educational Qualifications with specialization:

Signature of Head of Institution  
(with seal)

8. Subjects taught so far:

**Address for correspondence**

9. No. of refresher courses/workshops attended:

*Post your application form with DD to*

10. Experience (in years)  
Teaching :  
Research:  
Industry:

Dr.S.Anuradha,  
Coordinator for FDP on Information Theory and Coding-  
Applications  
Associate Professor  
Dept. of Electronics and Communication Engg, National  
Institute of Technology Warangal, WARANGAL - 506  
004, Telangana State, India.

11. Accommodation required: YES / NO

12. Do you belong to SC/ST: YES/NO

*Mail the scanned copies of filled-in and duly signed  
application form with DD to*

anuradha@nitw.ac.in

For more information visit: <http://nitw.ac.in/eict/>

**Declaration**

The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the FDP and shall attend the course for the entire duration. I also undertake the responsibility to inform the Coordinator in case, I am unable to attend the course.

For any enquiry contact:

Mobile: 8332969364, 9490334276

Land line: 0870-2462446

Place:

Date:

Signature of the applicant

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**Coordinator**

**Dr.S.Anuradha**  
Associate Professor  
Organized by

**Electronics & ICT Academy and**  
**Department of Electronics and Communication Engineering**

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