



FACULTY DEVELOPMENT PROGRAMME (FDP) ON

Data Analysis using Open Source Statistical Package “R” (27th November 2017 to 02nd December 2017)

**Organized by Velagapudi Ramakrishna Siddhartha Engineering College, Vijayawada
in association with National Institute of Technology, Warangal**

Under the scheme of Electronics & ICT Academy

(Sponsored by Ministry of Electronics and Information Technology (MeitY), GOI)



Preamble:

“Electronics & ICT Academy” was established at NIT Warangal in August 2015 with financial assistance from Ministry of Electronics and Information Technology (MeitY), GoI. The jurisdiction of this academy is Telangana, Andhra Pradesh, Andaman and Nicobar Islands, Goa, Puducherry and Karnataka states/UTs. The role of the academy is to offer faculty development programmes in standardized courses and emerging areas of Computer Science, Electronics, Information Communication Technologies; training & consultancy services for Industry; Curriculum development for Industry; CEP for working professionals; Advice and support for technical incubation and entrepreneurial activities.

As part of this initiative, a Faculty Development Programme (FDP) on **Data Analysis using Open Source Statistical Package “R”** is being conducted at VRSEC, Vijayawada to the Faculty members of engineering, MCA and other allied degree colleges.

Major Course Contents:

- Introduction to R and R Studio Data Coding
- Data Management
- Data Transformation
- Descriptive Statistics

It is more than a statistical package as it provides a wide variety of statistical (Linear and nonlinear Modeling, classical statistical tests, time series analysis, classification, clustering etc.)

Organized at:

The programme will be coordinated and conducted by Information Technology Department of Velagapudi Ramakrishna Siddhartha Engineering College (VRSEC), Vijayawada. The whole course is handled by academicians and industry experts.

Eligibility:

The programme is open to the *faculty of engineering colleges, MCA/BCA degree colleges and other allied disciplines* in Telangana, Andhra Pradesh, Karnataka states and Puducherry, Andaman and Nicobar Islands. Industry and other working professionals are also eligible.

Pre-Requisites: Basic Programming Concepts.

Registration Fee Particulars:

Faculty and Research Scholars from Telangana, Andhra Pradesh, Karnataka, Puducherry, Andaman and Nicobar Islands, Goa	Rs. 2500/-
Faculty of SC/ST Category from Telangana, Andhra Pradesh, Karnataka, Puducherry, Andaman and Nicobar Islands, Goa	Rs. 1250/- (SC/ ST participants should submit the copy of their caste certificate to claim the concession along with application form)
Industry Participants	Rs. 7500/-
Participants from other states	Rs. 8500/-

SC/ST concession is only for faculty of mentioned states.

Research Scholars are not eligible for SC/ST concession.

The participants need to send a crossed demand draft (DD) drawn in favor of “Director, NIT Warangal” and payable at SBH, NIT Warangal branch.

Accommodation:

No TA/DA will be paid to the participants. Accommodation is arranged for outstation participants at VRSEC, Vijayawada campus. Working Lunch, Tea & Snacks and Dinner would be provided during the training at VRSEC campus.

How to apply:

A filled-in form of application in the prescribed format duly signed and sponsored by appropriate authorities (along with demand draft) should reach the coordinator on or before the last date by post/courier. Participants are requested to write their name and contact number backside of demand draft. It is also mandatory to send scanned application form and demand draft through e-mail to hodit@vrsiddhartha.ac.in as selection will be intimated only through e-mail.

Selection Criteria:

Selection will be done based on *first-cum-first-serve basis* and the confirmed candidates will be notified by email. The maximum number of participants will be **50 (fifty)**. Additionally 10 participants from industry are allowed to participate. **The list of selected participants will be intimated through e-mail.** In case a candidate is not selected, the demand draft will be sent back. Candidates will be issued certificates on successful completion of the course along **with grade**. Reservations of seats are followed for selecting candidates as per GOI norms.

Important dates:

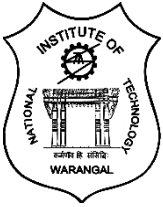
Last Date(Application & DD)	15 th November, 2017
Selection List by Email	22 nd November, 2017
Duration	27 th November to 02 nd December, 2017

About NIT Warangal:

National Institute of Technology, Warangal is the first among 17 RECs setup as joint venture of the Government of India and the state government. Over the years the college has established itself as a premier Institute imparting technical education of a very high standard leading to the B.Tech degrees in various branches of engineering, M.Tech. and Ph.D programs in various specializations. All B. Tech and M. Tech programmes of NIT Warangal are NBA accredited.

About VRSEC, Vijayawada:

Velagapudi Ramakrishna Siddhartha Engineering College also known as VRSEC, Vijayawada was established in 1977 as the first private self-financing engineering college in the state of united Andhra Pradesh sponsored by Siddhartha Academy of General and Technical Education(SAGTE), a society holding 15 institutions in the areas of engineering, medical, education, Dental, law, pharmacy, arts & science, nursing, hotel management and schools. The institute is offering seven UG programs and nine PG programs in addition to MBA and MCA Courses. The institute is accredited by NBA thrice prior to the accreditation under Outcome Based Education (OBE) in the years 2014 and 2016. Also the institute is accredited by NAAC with A grade for a period of five years with a CGPA of 3.17 on four point scale effective from 25-10-2013.



APPLICATION FORM
Electronics and ICT Academy-NITW
&
Department of IT - VRSEC

Faculty Development Programme on
Data Analysis using Open Source Statistical Package “R”
(27th November 2017 to 02nd December 2017)

1. Name:
2. Designation:
3. Institution:
4. E-mail:
Mobile:
5. DD No:
Bank:
Date:
6. Address for Correspondence

7. Educational Qualifications with specialization:

8. Subjects taught so far:

9. No. of refresher courses/workshops attended:
10. Experience (in years) :
Teaching: Research: Industry:

11. Do you belong to reservations SC/ST (YES/NO):

(If yes, please specify and attach a copy of caste certificate to claim the concession)

12. Do you require accommodation during Training:
Yes/ No

Declaration

The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the FDP and shall attend the course for the entire duration. I also undertake the responsibility to inform the Coordinator in case, I am unable to attend the course.

Place:

Date:

Signature of the applicant

SPONSORSHIP CERTIFICATE

Dr./Mr./Ms.

..... is an employee of our Institute/Organization and is hereby sponsored to participate in the FDP on **Data Analysis using Open Source Statistical Package “R”** sponsored by **Electronics & ICT Academy** during 27th November 2017 to 02nd December 2017 at VRSEC, Kanuru, Vijayawada - 520007

Place:

Date:

Signature of
Principal / Head of Institution(With seal)

Address for correspondence

Post your application form along with DD to

Dr. M. Suneetha
Training Coordinator for FDP on
Data Analysis using Open Source Statistical Package
“R”
Department of Information Technology
Velagapudi Ramakrishna Siddhartha Engineering
College
Kanuru,
Vijayawada – 520 007
Andhra Pradesh State, India

E-mail the scanned copies of filled-in and duly signed application form along with DD to
hodit@vrsiddhartha.ac.in

For more details about Electronics and ICT academy please visit: <http://nitw.ac.in/eict/>

For any enquiry please contact:

Tel.:+919032563468,

jaya1123@vrsiddhartha.ac.in

Email:

Coordinators

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