



## FACULTY DEVELOPMENT PROGRAMME (FDP)

ON

### CMOS INTEGRATED CIRCUITS FOR INSTRUMENTATION AND IOT APPLICATIONS

(22nd - 28th, January 2018)

Organized by

**Velagapudi Ramakrishna Siddhartha Engineering College,  
Vijayawada**

in association with

**E & ICT Academy, National Institute of Technology, Warangal.**

(Sponsored by Ministry of Electronics and Information Technology (MeitY), GOI)

#### Preamble:

"Electronics & ICT Academy" was set up at NIT Warangal with financial assistance from MeitY, Gol. The jurisdiction of this academy is Telangana, Andhra Pradesh, Karnataka, Goa, Puducherry and Andaman & Nicobar Islands. This academy role is to offer faculty development programmes in standardized courses and emerging areas of Electronics, Information Communication Technologies, training & consultancy services for Industry, Curriculum development for Industry, CEP for working professionals, Advice and support for technical incubation and entrepreneurial activities.

This faculty development programme (FDP) is devoted to enhance the knowledge on theoretical and practical aspects CMOS integrated circuits for instrumentation and IoT applications.

#### Major Course Contents:

- Overview of Industry/Academic instrumentation
- Power management ICs for portable instrumentation systems
- Instrumentation amplifiers
- Switched capacitor circuits and Comparators
- Applications of switched capacitor circuits
- Fundamentals of Data converters for data acquisition
- Pipe lined, SAR and Sigma delta Data converters
- Intelligent Instrumentation
- Sensors for IoT applications
- IoT applications

#### Faculty conducting this programme:

The programme will be conducted by the faculty members from NIT Warangal; Academicians in the concerned field from IITs/NITs/IITs are invited to deliver lectures in the programme. Speakers from industries are also expected to deliver as part of the course.

#### Eligibility:

The programme is open to the teachers of Engineering Colleges, MCA Colleges and other allied disciplines in Telangana, Andhra Pradesh, Karnataka, Goa, Puducherry, Andaman and Nicobar Islands. Industry personnel working in the concerned /allied discipline can also attend.

#### Accommodation:

Outstation participants will be provided with accommodation in the institute hostels. No TA will be paid for the participants.

#### Registration Fee Particulars:

Faculty and Research Scholars from Telangana, Andhra Pradesh, Karnataka, Puducherry, Andaman and Nicobar Islands, Goa	Rs. 2500/-
Faculty of SC/ST Category from Telangana, Andhra Pradesh, Karnataka, Puducherry, Andaman and Nicobar Islands, Goa	Rs. 1250/- (SC/ ST participants should submit the copy of their caste certificate to claim the concession along with application form)
Industry Participants	Rs. 7500/-
Participants from other states	Rs. 8500/-

SC/ST concession is only for faculty of mentioned states.  
Research Scholars are not eligible for SC/ST concession.

The entire registration fee is to be collected in the form of DDs/online transfer using the following details:

DD Details	Online Transfer Details
Demand Draft in favor of "Director, NIT Warangal" payable at any bank in Warangal	Account Name: Electronics & ICT Academy NITW Account No: 62423775910 IFSC: SBIN0020149

#### How to apply:

A filled in form of application in the prescribed format duly signed and sponsored by appropriate authorities (along with demand draft) should reach the coordinator by speed-post. It is also mandatory to send scanned application form and demand draft through e-mail to [hodeie@vrsiddhartha.ac.in](mailto:hodeie@vrsiddhartha.ac.in) as selection will be intimated only through mail.

#### Selection Criteria:

Selection will be done based on first-come-first-serve basis to a maximum number of 50 (fifty). Additionally 10 participants from industry are allowed to participate. The list of selected participants will be intimated through e-mail. In case a candidate is not selected, the DD will be sent back. Candidates will be issued certificates on successful completion of the course. Reservations are followed for selecting candidates as per GOI norms.

#### Important dates:

Last date (Application & DD)	18/01/2018
Selection List by E- mail	20/01/2018
Duration	22 <sup>nd</sup> – 28 <sup>th</sup> January 2018

#### About NIT Warangal:

**National Institute of Technology, Warangal** is the first among 17 RECs setup as joint venture of the Government of India and the state government. Over the years the college has established itself as a premier Institute imparting technical education of a very high standard leading to the B.Tech degrees in various branches of engineering, M.Tech. and Ph.D programs in various specializations. All B. Tech and M. Tech programmes of NIT Warangal are NBA accredited.

#### About VRSEC, Vijayawada:

**Velagapudi Ramakrishna Siddhartha Engineering College (VRSEC)** was established in the year 1977 as the first self-financing engineering college in the state of A.P. It is located in a vast expanse of 24.05 acres of land on the outskirts of Vijayawada city at a distance of about 6Kms from the city centre. The college offers 7 undergraduate programmes in various branches of engineering and post graduate programmes in 10 specializations. The college has been accredited four times by National Board of Accreditation (NBA) of All India Council for Technical Education (AICTE), New Delhi in respect of all Engineering disciplines and also certified for ISO 9001:2008. Autonomous status was conferred by UGC in the year 2006 and it is affiliated to Jawaharlal Nehru Technological University, Kakinada, AP. It is one among the top 16 engineering colleges that were selected under World Bank aid for R&D and PG enhancement programme called TEQIP –II (S.C.1.2) by MHRD, Govt. of India. In pursuit of its vision to nurture excellence in engineering, the college has embraced Outcome Based Education since 2009.



## APPLICATION FORM

### **FACULTY DEVELOPMENT PROGRAMME (FDP) ON**

### **CMOS INTEGRATED CIRCUITS FOR INSTRUMENTATION AND IOT APPLICATIONS**

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1. Name :
2. Designation :
3. Institution :
4. Email :
5. Mobile No :
6. DD No:                      Bank:                      Date:

#### **SPONSORSHIP CERTIFICATE**

Dr. /Mr. /Ms. .... is an employee of our Institute/Organization and is hereby sponsored to participate in the FDP on “**CMOS Integrated Circuits for Instrumentation and IoT Applications**”, sponsored by Electronics & ICT Academy during 22<sup>nd</sup> – 28<sup>th</sup> January, 2018 at VRSEC, Vijayawada

Amount:

7. Address for Correspondence:

Signature of Head of Institution  
(with seal)

8. Educational Qualification:
9. Subjects taught so far:

#### **Address for correspondence**

*Post your application form with DD to*

**Dr. G. N. Swamy,  
Professor & Head  
Department of Electronics &  
Instrumentation Engineering,  
Velagapudi Ramakrishna Siddhartha  
Engineering College  
Kanuru, VIJAYAWADA-520007,  
Andhra Pradesh, India.**

- 10.No. of refresher courses/workshops attended:
11. Experience (in years):  
Teaching:                      Research:                      Industry:

12. Accommodation required:      YES / NO
13. Do you belong to SC/ST :      YES / NO

(If yes, please specify and attach a copy of caste certificate to claim the concession)

*E-mail the scanned copies of filled-in and duly signed application form along with DD to*

[hodeie@vrsiddhartha.ac.in](mailto:hodeie@vrsiddhartha.ac.in)

*For more details about Electronics & ICT Academy, NIT, Warangal, please visit:  
<https://nitw.ac.in/eict>*

*For more enquiries please contact:*

**Mobile: 7013528898,  
9491649264**

#### **Declaration**

The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the FDP and shall attend the course for the entire duration. I also undertake the responsibility to inform the Coordinator in case, I am unable to attend the course.

Signature of the Applicant

### **Coordinators**

**Dr. P. Srihari Rao  
Associate Professor  
Department of ECE,  
NIT Warangal**

**Dr. G. N. Swamy  
Professor & Head  
Department of EIE,  
VRSEC, VIJAYAWADA**

