



FACULTY DEVELOPMENT PROGRAMME (FDP)
ON
CMOS ANALOG IC DESIGN
(13th - 17th, November 2017)



Organized by
**Velagapudi Ramakrishna Siddhartha Engineering
College, Vijayawada**
in association with NIT, Warangal
Under the scheme of Electronics & ICT Academy
(Sponsored by Ministry of Electronics and Information Technology (MeitY), GOI)

Preamble:

"Electronics & ICT Academy" was set up at NIT Warangal with financial assistance from MeitY, Govt. of India. The jurisdiction of this academy is Telangana, Andhra Pradesh, Karnataka, Goa, Puduchery and Andaman & Nicobar Islands. This academy role is to offer faculty development programmes in standardized courses and emerging areas of Electronics, Information Communication Technologies, training & consultancy services for Industry, Curriculum development for Industry CEP for working professionals, Advice and support for technical incubation and entrepreneurial activities.

This faculty development programme (FDP) is devoted to enhance the knowledge on theoretical and practical aspects analog IC design.

Major Course Contents:

- Overview of IC Design
- Aspects of IC Design
- Introduction to device characteristics
- Biasing styles
- Single-Stage Amplifiers
- Cascode Amplifier Gain Boosting Techniques
- Differential amplifiers and Current Mirrors
- Frequency Response of amplifiers and Noise
- Op-Amp Design and Compensation
- Mixed Signal Design/ RF IC Design

Faculty conducting this programme:

The programme will be conducted by the faculty members from NIT Warangal. Academicians and Industrialists in the concerned field from Reputed Institutes are invited to deliver lectures in the programme.

Eligibility: The programme is open to the *teachers of engineering colleges and research scholars* in Telangana, Andhra Pradesh, Karnataka states and Puducherry, Andaman and Nicobar islands and Goa UTs. Industry personnel working in the concerned/allied discipline can also attend.

Registration Fee Particulars:

Faculty and Research Scholars from Telangana, Andhra Pradesh, Karnataka, Puducherry, Andaman and Nicobar Islands, Goa	Rs. 2500/-
Faculty of SC/ST Category from Telangana, Andhra Pradesh, Karnataka, Puducherry, Andaman and Nicobar Islands, Goa	Rs. 1250/- (SC/ ST participants should submit the copy of their caste certificate to claim the concession along with application form)
Industry Participants	Rs. 7500/-
Participants from other states	Rs. 8500/-

The participants need to send a crossed demand draft (DD) drawn in favour of "Director, NIT Warangal" and payable at SBH, NIT Warangal branch.

Accommodation:

Outstation participants will be provided with accommodation in the institute hostels. No TA will be paid for the participants.

How to apply:

A filled in form of application in the prescribed format duly signed and sponsored by appropriate authorities (along with demand draft) should reach the coordinator by speed-post. It is also mandatory to send scanned application form and demand draft through e-mail to hodece@vrsiddhartha.ac.in as selection will be intimated only through mail.

Selection Criteria:

Selection will be done based on *first-cum-first-serve basis* and the confirmed candidates will be notified immediately. The maximum number of participants will be **50 (fifty)**. Additionally 10 participants from industry are allowed to participate. The list of selected participants will be notified in the institute web site www.nitw.ac.in and also will be sent to their personal e-mail ids. In case a candidate is not selected, the demand draft will be sent back. A test will be conducted at the end of the course. Candidates will be issued certificates on successful completion of the course along with grade. Reservations are followed for selecting candidates as per GOI norms.

Important dates:

Last date for submission of application: **30/10/2017**

Selection-list intimation/display before: **01/11/2017**

Duration of the Program : **13th – 17th Nov 2017**

About NIT, Warangal:

National Institute of Technology, Warangal is the first among 17 RECs setup as joint venture of the Government of India and the state government. Over the years the college has established itself as a premier Institute imparting technical education of a very high standard leading to the B.Tech degrees in various branches of engineering, M.Tech. and Ph.D programs in various specializations. All B. Tech and M. Tech programmes of NIT Warangal are NBA accredited.

About the Institute:

Velagapudi Ramakrishna Siddhartha Engineering College (VRSEC) was established in the year 1977 as the first self-financing engineering college in the state of A.P. It is located in a vast expanse of 24.05 acres of land on the outskirts of Vijayawada city at a distance of about 6Kms from the city centre. The college offers 7 undergraduate programmes in various branches of engineering and post graduate programmes in 10 specializations. The college has been accredited four times by National Board of Accreditation (NBA) of All India Council for Technical Education (AICTE), New Delhi in respect of all Engineering disciplines and also certified for ISO 9001:2008. Autonomous status was conferred by UGC in the year 2006 and it is affiliated to Jawaharlal Nehru Technological University, Kakinada, AP. It is one among the top 16 engineering colleges that were selected under World Bank aid for R&D and PG enhancement programme called TEQIP –II (S.C.1.2) by MHRD, Govt. of India. In pursuit of its vision to nurture excellence in engineering, the college has embraced Outcome Based Education since 2009.

FORMAT OF APPLICATION
Electronics and ICT Academy, NIT Warangal and
Velagapudi Ramakrishna Siddhartha Engineering College, Vijayawada
Faculty Development Programme on
CMOS ANALOG IC DESIGN
(13th - 17th, November 2017)

1. Name :
2. Designation :
3. Institution :
4. E-mail :
5. MobileNo. :
6. DDNo :

SPONSORSHIP CERTIFICATE

Dr/Mr/Ms.
..... is an employee of our Institute/Organization and is hereby sponsored to participate in the FDP on **CMOS Analog IC Design** sponsored by **Electronics & ICT Academy** during 13th – 17th Nov, 2017 at Velagapudi Ramakrishna Siddhartha Engineering College, Vijayawada.

Bank: Date:

7. Address for Correspondence

Place:

Date:

Signature of Head of Institution

(with seal)

8. Educational Qualifications with specialization:

9. Subjects taught so far:

- 10.No. of refresher courses/workshops attended:

Address for correspondence:

Post your application form with DD to

Dr. K. Sri Rama Krishna,
Professor & Head
Department of Electronics & Comm. Engineering,
Velagapudi Ramakrishna Siddhartha Engineering College
Kanuru, VIJAYAWADA-520007,
Andhra Pradesh, India.

11. Experience (in years)

Teaching :
Research :
Industry :

Mail the scanned copies of filled-in and duly signed application form with DD to

hodece@vrsiddhartha.ac.in

- 12.Accommodationrequired: YES /NO

- 13.Do you belongtoSC/ST: YES/NO

For more information visit:

<http://nitw.ac.in/eict/>

Declaration

The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the FDP and shall attend the course for the entire duration. I also undertake the responsibility to inform the Coordinator in case, I am unable to attend the course.

For any enquiry contact: Mobile:

9642086380, 9491649264

Place:

Date:

Signature of theapplicant

Coordinators

Dr. P. Sreehari Rao
Professor
Department of ECE,
NIT Warangal

Dr. K. Sri Rama Krishna
Professor & Head
Department of ECE,
VRSEC, VIJAYAWADA