FACULTY DEVELOPMENT PROGRAMME(FDP)
ON
BIO INSPIRED ALGORITHMS IN ENGINEERING OPTIMIZATION
(19th – 28th November, 2018)
Organized by
E & ICT Academy, NIT Warangal
(Sponsored by Ministry of Electronics and Information Technology (MeitY), GOI)

Preamble:

"Electronics & ICT Academy" was set up at NIT Warangal with financial assistance from MeitY, GoI. The jurisdiction of this academy is Telangana, Andhra Pradesh, Karnataka, Goa, Puducherry and Andaman & Nicobar Islands. This academy role is to offer faculty development programmes in standardized courses and emerging areas of Electronics, Information Communication Technologies, training & consultancy services for Industry, Curriculum development for Industry, CEP for working professionals, Advice and support for technical incubation and entrepreneurial activities.

The goal FDP on above topic is for the Faculty and scientists currently doing research and development in field of VLSI and related signal processing. The physics based modeling of various semiconductor devices is an important area of research for the growth and development of semiconductor science and technology. While analytical modeling gives the physical inside of the device characteristics, TCAD/ TSUPREMO simulations can provide the first hand information of various advanced semiconductor devices (DGMOS FET/FinFET/TFET) without going through complex mathematical modeling and practical fabrication and experimentation. The course will also cover MEMS and VLSI Inter connection.

Major Course Contents:

- Intelligent systems design
- Genetic Algorithms
- Particle swarm Optimization
- Differential Evolution
- Bat Algorithm
- Teacher Learning based Optimization Algorithm
- Hybridization Mechanism
- Neural Networks

Applications
- DC Motor Speed Control
- Active suspension system design
- Robotic path Planning
- Channel Equalization
- System Identification
- Image processing
- Wireless Sensor Networks

Eligibility:

The program is open to the Faculty of Engineering Colleges, MCA Colleges and other allied disciplines in India. Industry personnel working in the concerned /allied discipline can also attend.

Faculty conducting this programme:

The programme will be conducted by the faculty members from NIT Warangal; Academicians in the concerned field from IITs/NITs/IIITs are invited to deliver lectures in the programme. Speakers from industries are also expected to deliver as part of the course.

Registration Fee Particulars:

Faculty and Research Scholars	Rs. 4000/-
Faculty of SC/ST Category	Rs. 3000/- (SC/ ST participants should submit the copy of their caste certificate to claim the concession along with application form)
Industry Participants	Rs. 12000/-

SC/ST concession is only for faculty of mentioned states. Research Scholars are not eligible for SC/ST concession.
The entire registration fee is to be collected in the form of DDs/online transfer using the following details:

DD Details	Online Transfer Details
Demand Draft in favor of ''Director, NIT Warangal'' payable at any bank in Warangal	Account Name: Electronics & ICT Academy NITW Account No: 62423775910 IFSC: SBIN0020149

Accommodation:

All the selected participants will be provided FREE boarding & lodging in the institute guest house. No TA will be paid for the participants

How to apply:

A filled in form of application in the prescribed format duly signed and sponsored by appropriate authorities (along with demand draft) should reach the coordinator by speed-post. It is also mandatory to send scanned application form and demand draft through e-mail to ravikumar@nitw.ac.in as selection will be intimated only through mail.

Selection Criteria:

Selection will be done based on first-come-first-serve basis to a maximum number of 50 (fifty). Additionally 10 participants from industry are allowed to participate. The list of selected participants will be intimated through e-mail. In case a candidate is not selected, the DD will be sent back. Candidates will be issued satisfactory certificates on successful completion of the course. Reservations are followed for selecting candidates as per GOI norms.

Important dates:

Last date (Application & DD)	12/11/2018
Selection List by E- mail	13/11/2018
Duration	19th – 28th November, 2018

About NIT Warangal & Department:

National Institute of Technology, Warangal is the first among 17 RECs setup as joint venture of the Government of India and the state government. Over the years the college has established itself as a premier Institute imparting technical education of a very high standard leading to the B.Tech degrees in various branches of engineering, M.Tech. and Ph.D programs in various specializations. All B. Tech and M. Tech programmes of NIT Warangal are NBA accredited.

The Department of Electronics and Communication Engineering offers an Undergraduate program in Electronics and communication Engineering and three Postgraduate programs in EI, VLSI, and ACS Specializations. The Department has experienced faculty and well-established laboratories. The Department has liaison with reputed industries and R&D organizations like DRDO, ISRO ECIL, Analog Devices Bangalore and C-DAC. Department conducts various sponsored programmes throughout the year.

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1. Name   :

2. Designation   :

3. Institution   :

4. Email   :

5. DD No: Bank: Date:

    Amount:

6.   Address for Correspondence:

7.   Educational Qualification:

8.   Subjects taught so far:

9. No. of refresher courses/workshops attended:

10. Experience (in years):

     Teaching: Research: Industry:

11.   Accommodation required: YES / NO

12. Do you belong to SC/ST :   YES / NO

(If yes, please specify and attach a copy of caste certificate to claim the concession)

Declaration

The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the FDP and shall attend the course for the entire duration. I also undertake the responsibility to inform the Coordinator in case, I am unable to attend the course.

Signature of the Applicant

SPONSORSHIP CERTIFICATE

Dr. /Mr. /Ms ....is an employee of our Institute/Organization and is hereby sponsored to participate in the FDP on **"Bio Inspired Algorithms in Engineering Optimization"**, sponsored by Electronics & ICT Academy during **19th – 28th November, 2018** at NIT Warangal.

Signature of Head of Institution
(with seal)

Address for correspondence
Post your application form with DD to

Dr. J. Ravi Kumar,
Associate Professor
Department of ECE,
National Institute of Technology Warangal,
WARANGAL - 506 004, Telangana State, India.

E-mail the scanned copies of filled-in and duly signed application form along with DD to
ravikumar@nitw.ac.in

For more details about Electronics & ICT Academy, NIT, Warangal, please visit: *https://nitw.ac.in/eict*

For more enquiries please contact**:**

Mobile: 8332969363

Coordinator

Dr. J. Ravi Kumar & Dr. P. Muralidhar
Associate Professor(s)
Department of ECE,
NIT Warangal,
WARANGAL - 506 004,
Telangana State, India.