



E&ICT ACADEMY

NATIONAL INSTITUTE OF TECHNOLOGY

WARANGAL – 506004, TELANGANA STATE – INDIA

FDP on “Advances in Electronic Device Modeling & Simulation Studies (AEDMS)”

From July 5th to July 13th 2021

Lecture Schedule

Date	10.00AM-11.50AM			12.00PM– 1.00PM		2.00PM-3.50PM		4.00-5.00PM	
05.07.2021	9.30AM – 10.00AM Inauguration	Trends in VLSI (Prof. N. S. Murthy)		Modelling and Simulation - Basics (Dr. Maheshwaram Satish, NITW)		CMOS VLSI Building Blocks & Design Issues Mr. Rajesh Kumar, SCL-SE			
06.07.2021	Analog Design (Front end-1: components available-active & Passive) H. S. Jatana, SCL-SG		T e a c h B r e a k	Analog Design (Front end-2: components available-active & Passive) H. S. Jatana, SCL-SG	L u n c h B r e a k	Analog Design Lab – I Session Mr. Srikar, MNC Industry VLSI tool Expert Dr. I.S.Rao	T e a c h B r e a k		
07.07.2021	Analog Layout issue & Challenges Design kit (How to use) H. S. Jatana, SCL-SG					Low power Design Concepts & Introduction to Functionality of simulator Dr. I. Sreenivasa Rao		Low power Design Concepts & Introduction to Functionality of simulator Dr. I. Sreenivasa Rao	
08.07.2021	A review on Memristors-I Prof. Gaurav Trivedi, IITG					Analog Design Lab-II session Dr. I.S.Rao/ Dr. M.Chaitanya, NIT Delhi		Analog Design Lab-II session Dr. I.S.Rao/ Dr. M.Chaitanya, NIT Delhi	
09.07.2021	A review on Memristors-II Prof. Gaurav Trivedi, IITG					Memristor using OOMMF Dr. Shivam Verma, IIT-BHU		Memristor using OOMMF Dr. Shivam Verma, IIT-BHU	
10.07.2021	MOSFET Scaling – Issues & Solutions Dr. Maheshwaram Satish, NITW					Compact device modeling using VerilogA Dr. M. Satish		Compact device modeling using VerilogA Dr. M. Satish	
12.07.2021	Doublegate MOSFET Modeling Dr. Narendar, NITW					Demo: Visual TCAD Dr. M. Satish		Demo: Visual TCAD Dr. M. Satish	
13.07.2021	Research - FinFET Dr. V. Narendar, NITW					Research - NWFET Dr. M Satish, NITW		Research - NWFET	Valedictory

Speakers:

- 1) Dr. Maheshwaram Satish, Assistant Professor, Dept of ECE, NITW
- 2) Prof. N. S. Murthy, Rtd- Prof, NITW
- 3) Mr. Rajesh Kumar, SCL-SE
- 4) Mr. H.S. Jatana, SCL-SG, Group Head DGP, SCL, Mohali
- 5) Dr. I. Sreenivasa Rao, GITAM University

- 6) Dr. M. Chaitanya, NIT Delhi
- 7) Prof. Gaurav Trivedi, Dept of ECE, IITG
- 8) Dr. V. Narendar, Assistant Professor, Dept of ECE, NITW
- 9) Dr. Shivam Verma, Dept. of Electronics Engg., IIT -

Coordinators:

Dr. Pallavi Khare

Dr. Maheshwaram Satish