



NATIONAL INSTITUTE OF TECHNOLOGY
WARANGAL – 506 004 (Telangana State)
DeitY, HRD division, Ministry of ICT, Govt. of India SPONSORED
Faculty Development Programme (FDP)
On
Signal Processing for Communications

Organized by

Electronics & ICT Academy
and Department of Electronics and Communication Engineering
(4th Jan 2016– 8th Jan 2016)

Preamble:

"Electronics & ICT Academy" is being set up at NIT Warangal with financial assistance from DeitY, MCIT, Gol. The jurisdiction of this academy is Telangana, Andhra Pradesh, Karnataka States and Goa. The role of academy is to offer faculty development programmes in standardized courses and emerging areas of Electronics, Information Communication Technologies; training & consultancy services for Industry; Curriculum development for Industry; CEP for working professionals; Advice and support for technical incubation and entrepreneurial activities.

With the recent exploding research interest in wireless communications, the application of signal processing to the communication area is becoming increasingly important. Indeed, it is the advances in signal processing technology that make most of today's wireless communications possible and hold the key to future services. The application of signal processing techniques to wireless communications is an emerging area that has recently achieved dramatic and important results and holds the potential for even greater results in the future as an increasing number of researchers from the signal processing and communication areas participate in this expanding field. This course focuses on recent developments and research outcomes addressing the related theoretical and practical aspects on signal processing advances in wireless communications.

Major Course Contents:

- Review of Signals and Systems
- Transform Techniques
- FIR and IIR filters
- Realization of Filters
- DSP Processors
- Applications
 - Speech communication
 - image processing
 - Radar signals
 - OFDM- related techniques, equalization, multiuser detection.

Faculty conducting this programme:

The programme will be conducted by the faculty members from **NIT Warangal**. Academicians in the concerned field from **IITs/NITs/IIITs** are invited to deliver lectures in the programme. Speakers from **industries** are also expected to deliver as part of the course.

Eligibility: The programme is open to the **teachers of engineering college from Electronics and communication Engineering, Electrical Engineering, and other allied disciplines** in Telangana, Andhra Pradesh, Karnataka states and Goa. Industry personnel working in the concerned/allied discipline can also attend.

Registration Fee Particulars:
Faculty members (Rs 2000/- Only) and
Industry participants (Rs 6000/-Only)

The participants need to send a crossed demand draft(DD) drawn in favour of "Director, NIT Warangal" and payable at SBH, NIT Warangal branch.

Accommodation:

All the selected participants will be provided FREE boarding & lodging in the institute guest house. No TA will be paid for the participants.

How to apply:

A filled in form of application in the prescribed format duly signed and sponsored by appropriate authorities (along with demand draft) should reach the coordinator by speed-post. It is also mandatory to send scanned application form and demand draft through e-mail to spc2016@nitw.ac.in as selection will be intimated only through mail.

Selection Criteria:

Selection will be done based on **first-cum-first-serve basis** and the confirmed candidates will be notified immediately. The maximum number of participants will be **50 (fifty)**. Additionally 10 participants from industry are allowed to participate. The list of selected participants will be notified in the institute web site www.nitw.ac.in and also will be sent to their personal *e-mail* ids. In case a candidate is not selected, the demand draft will be returned. A test will be conducted at the end of the course. Candidates will be issued certificates on successful completion of the course along with grade. Reservations are followed for selecting candidates as per GOI norms.

Important dates:

Last date for submission of application: 21.12.2015

Selection-list intimation/display before: 23.12.2015

Duration of Program: 4th January 2016– 8th January 2016

About the Institute, department and Warangal:

National Institute of Technology (formerly Regional Engineering College), Warangal is the first among 17 RECs setup as joint venture of the Government of India and the state government. Over the years the college has established itself as a premier Institution imparting technical education of a very high standard leading to the B.Tech degrees in various branches of engineering and M.Tech. and Ph.D programs in various specializations.

The Department of Electronics and Communication Engineering offers B.Tech. course in ECE, M.Tech courses in Advanced Communication Systems (ACS), VLSI design and Electronic Instrumentation. The Department has experienced faculty with good publications and well-established laboratories. The Department has liaison with reputed industries and R&D organizations.

Warangal is known for its rich historical and cultural heritage. It is situated at a distance of 140Km. from Hyderabad. Warangal is well connected by rail and road. National Institute of Technology campus is 2 Km. away from Kazipet station and 12Km. away from Warangal station.

FORMAT OF APPLICATION
Electronics and ICT Academy
Faculty Development Programme
on
Signal Processing for Communications
(4th Jan 2016– 8th Jan 2016)

1. Name :
2. Designation :
3. Institution :
4. email:
5. DD No: Bank: Date:
6. Address for Correspondence

7. Educational Qualifications with specialization:
8. Subjects taught so far:
9. No. of refresher courses/workshops attended:

10. Experience (in years)
Teaching :
Research:
Industry:
11. Accommodation required: YES / NO
12. Do you belong to SC/ST: YES/NO

Declaration

The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the FDP and shall attend the course for the entire duration. I also undertake the responsibility to inform the Coordinator in case, I am unable to attend the course.

Place: _____
Date: _____ Signature of the applicant

SPONSORSHIP CERTIFICATE

Dr/Mr/Ms. is
..... is
an employee of our Institute/Organization and is hereby sponsored to participate in the FDP on signal processing for communications sponsored by **Electronics & ICT Academy** during 4th January 2016-8th January 2016 at National Institute of Technology, Warangal

Place:
Date:

Signature of Head of Institution
(with seal)

Address for correspondence

Post your application form with DD to
Dr.S.Anuradha, Dr.T.Kishore Kumar
Dept. of Electronics and Communication Engg,
National Institute of Technology Warangal,
WARANGAL - 506 004, Telangana State, India.

Mail the scanned copies of filled-in and duly signed application form with DD to
spc2016@nitw.ac.in
For more information visit: <http://nitw.ac.in/eict/>

For any enquiry contact:
Mobile: 8332969364
Land line: 0870-2462446

Coordinators

Dr.T.Kishore Kumar,Dr.S.Anuradha

Organized by

Electronics & ICT Academy and
Department of Computer Science and Engineering
NATIONAL INSTITUTE OF TECHNOLOGY
WARANGAL – 506 004 (Telangana State)